

WEST Search History

DATE: Friday, March 09, 2007

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L29	(5671149 6172518 5914873 6885563)[pn]	4
<input type="checkbox"/>	L28	(5712790 5825662)[pn]	2
<input type="checkbox"/>	L27	L26 and slack	8
<input type="checkbox"/>	L26	l2 and (operating near3 voltage)	1768
<input type="checkbox"/>	L25	L24 and slack	9
<input type="checkbox"/>	L24	l2 and ((first and second) near3 (supply or voltage))	2784
<input type="checkbox"/>	L23	(6038386 5612892)[pn] or 20040145955	3
<input type="checkbox"/>	L22	(6574778 6269467)[pn]	2
<input type="checkbox"/>	L21	L20 and slack	1
<input type="checkbox"/>	L20	L2 and (voltage near2 (switch or switches))	1069
<input type="checkbox"/>	L19	L7 and voltage	16
<input type="checkbox"/>	L18	L17 and slack	69
<input type="checkbox"/>	L17	l2 and ((control\$ or variable or regulat\$) near5 voltage)	7274
<input type="checkbox"/>	L16	L13 and slack	11
<input type="checkbox"/>	L15	L14 and slack	0
<input type="checkbox"/>	L14	L13 and (operating near3 voltage)	245
<input type="checkbox"/>	L13	l2 and (voltage near2 regulator)	998
<input type="checkbox"/>	L12	L11 and voltage	49
<input type="checkbox"/>	L11	L10 and slack	102
<input type="checkbox"/>	L10	l2 and (timing near2 analysis)	565
<input type="checkbox"/>	L9	L8 and slack	5
<input type="checkbox"/>	L8	l2 and (assign\$ with voltage)	223
<input type="checkbox"/>	L7	l2 and (slack with minimum)	58
<input type="checkbox"/>	L6	l3 and slack	4
<input type="checkbox"/>	L5	L4 and slack	0
<input type="checkbox"/>	L4	L2 and ((blocks near3 active) same path)	53
<input type="checkbox"/>	L3	L2 and (blocks near3 active)	576
<input type="checkbox"/>	L2	(pld or "programmable logic device" or fpga or "field programmable gate array")	46812
<input type="checkbox"/>	L1	716/\$[ccls]	11700

WEST Search History

DATE: Friday, March 09, 2007

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L28	l27 and ("voltage switch\$")[clm]	3
<input type="checkbox"/>	L27	l10 and l18 and l19	176
<input type="checkbox"/>	L26	l10 and (control\$ with "voltage regulator")[clm]	10
<input type="checkbox"/>	L25	l10 and (select\$ with "operating voltage")[clm]	7
<input type="checkbox"/>	L24	L23 and variable[clm]	1
<input type="checkbox"/>	L23	l10 and "voltage regulator"[clm]	23
<input type="checkbox"/>	L22	L21 and l16	0
<input type="checkbox"/>	L21	l10 and ((voltage or supply) adj2 terminal)[clm]	40
<input type="checkbox"/>	L20	l10 and l11 and l15 and l18 and l19	0
<input type="checkbox"/>	L19	(second adj2 (supply or voltage))[clm]	67072
<input type="checkbox"/>	L18	(first adj2 (supply or voltage))[clm]	66745
<input type="checkbox"/>	L17	(enabl\$ with assignment)[clm]	457
<input type="checkbox"/>	L16	(minimum with slack)[clm]	136
<input type="checkbox"/>	L15	slack[clm]	6129
<input type="checkbox"/>	L14	L13 and path[clm]	9
<input type="checkbox"/>	L13	"active blocks"[clm]	77
<input type="checkbox"/>	L12	"active blocks"	1392
<input type="checkbox"/>	L11	"timing analysis"[clm]	454
<input type="checkbox"/>	L10	(pld or "programmable logic device")[clm]	3122
<input type="checkbox"/>	L9	(operat\$ with (pld or "programmable logic device"))[clm]	511
<input type="checkbox"/>	L8	L7 and slack	0
<input type="checkbox"/>	L7	l1 and L6	38
<input type="checkbox"/>	L6	326/80[ccls]	480
<input type="checkbox"/>	L5	L4 and slack	28
<input type="checkbox"/>	L4	l1 and (select\$ with voltage)	3779
<input type="checkbox"/>	L3	L2 and slack	6
<input type="checkbox"/>	L2	l1 and (enabl\$ with voltage)	1784
<input type="checkbox"/>	L1	((pld or "programmable logic device" or fpga or "field programmable gate array"))	46812

END OF SEARCH HISTORY